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27076 7590 09/18/2006

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Ayesha J. Shaikh		(Depositor's name)
		(Signature)
December 13, 2006		(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/699,256	10/30/2003	Chih-Chen Cho	500982.02.(30016/US/2)	9603

TITLE OF INVENTION: METHODS FOR MAKING SEMICONDUCTOR STRUCTURES HAVING HIGH-SPEED AREAS AND HIGH-DENSITY AREAS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	12/18/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS	12/19/2006 EHAILE2 00000077 10699256		
THOMAS, TONIAE M	2822	438-233000	61 FC:1531	1400.00 OP 200.00 ND	6.00 OP

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(A) NAME OF ASSIGNEE

Micron Technology, Inc.

Boise, Idaho

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Authorized Signature Kimton N. Eng

Date December 13, 2006

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Registration No. 43,605

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